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(54) Electrode structure for semiconductor device, method for forming the same, and mounted body including semiconductor device

(57) An electrode structure for a semiconductor device and a method for forming the electrode structure, and a mounted body including the semiconductor device are provided in which the semiconductor device can be easily connected to a circuit board with high reliability. An aluminum electrode (2) is formed on an IC substrate (1). A passivation film (5) is formed on the IC substrate (1) so as to cover the peripheral portion of the aluminum electrode (2). A bump electrode (3) is formed on the aluminum electrode (2) by a wire bonding method. An aluminum oxide film (4) is formed on the surface of the aluminum electrode (2) that is exposed around the bump electrode (3). A conductive adhesive (8) is applied as a bonding layer to the tip portion of the bump electrode (3) of the semiconductor device by a transfer method or a printing method. The semiconductor device is aligned in the face-down state in such a manner that the bump electrode (3) abuts on a terminal electrode (7) of a circuit board (6), and is provided on a circuit board (14). In this state, the conductive adhesive (8) is hardened. A gap between the IC substrate (1) and the circuit board (6) is filled with an insulating resin (9).

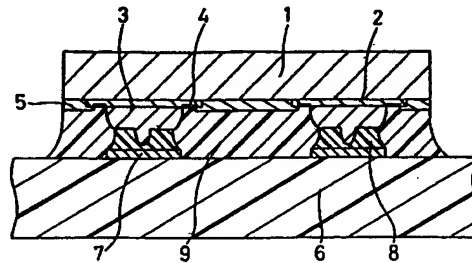


FIG. 2

EP 0 753 890 A2

## Description

The present invention relates to an electrode structure for a semiconductor device mounted on a circuit board and a method for forming the electrode structure, and a mounted body including the semiconductor device, and more particularly to an electrode structure for a semiconductor device mounted on a circuit board in the face-down state and a method for forming the electrode structure, and a mounted body including the semiconductor device.

Conventionally, a semiconductor device is often mounted on a circuit board by soldering. In recent years, however, the package for the semiconductor device has been small-sized and the number of connecting terminals has been increased. Consequently, the space between the connecting terminals has been reduced. Thus, it has become gradually harder to apply the soldering technique according to the prior art.

A method for directly attaching the semiconductor device onto the circuit board to reduce the mounting area so that the circuit board can be used efficiently has been proposed. In particular, the following mounting method is considered effective because the mechanical strength obtained after connecting the semiconductor device to the circuit board is great and direct connection can be performed (for example, "Microelectronics Packaging Handbook ; Nikkei BP published on March 27, 1991"). According to the above-mentioned method, a semiconductor device having an electrode structure is manufactured in which a deposited film such as an adherence metal or a diffusion preventive metal is formed on an aluminum electrode in advance when connecting the semiconductor device to the circuit board and a bump electrode made of a solder is formed on the deposited film by a plating method, and the semiconductor device is provided on the circuit board in the face-down state and heated at a high temperature so that the solder (bump electrode) and the terminal electrode of the circuit board are fused.

According to this method for forming the bump electrode, the electrode structure is complicated so that a lot of steps are necessary. For this reason, a method for easily forming the bump electrode on the aluminum electrode of the semiconductor device by using the wire bonding technique has been proposed (for example, Japanese Unexamined Patent Application No. 49-52973).

An example of the electrode structure for a semiconductor device and a method for forming the electrode structure and the mounted body including the semiconductor device according to the prior art will be described below with reference to the drawings. Fig. 3 is a sectional view showing the electrode structure for the semiconductor device according to the prior art. Fig. 4 is a sectional view showing the mounted body including the semiconductor device according to the prior art.

As shown in Fig. 3, an aluminum electrode 11 is formed on an IC substrate 10. A passivation film 13 is

formed on the IC substrate 10 so as to cover the peripheral portion of the aluminum electrode 11. A bump electrode 12 is formed on the aluminum electrode 11 by a wire bonding method.

As shown in Fig. 4, the semiconductor device having the bump electrode 12 is aligned in the face-down state in such a manner that the bump electrode 12 closely comes in contact with a terminal electrode 15 of a circuit board 14, and is provided on the circuit board 14. The bump electrode 12 and the terminal electrode 15 are fused together by thermo-compression bonding or ultrasonic welding. Thus, a mounted body including the semiconductor device is obtained.

However, the semiconductor device having the above-mentioned electrode structure and the mounted body including the semiconductor device have the following problems.

More specifically, the aluminum electrode is exposed around the bump electrode. For this reason, if water exists in the exposed aluminum electrode portion, there is a possibility that aluminum flows out in a comparatively short time so that the semiconductor device is not electrically conducted to the circuit board. In order to enhance the reliability of a junction, it has been proposed to fill a gap between the semiconductor device and the circuit board with an insulating resin. In this case, however, there is a possibility that impurity ions such as chlorine contained in the insulating resin promote the corrosion of the aluminum electrode so that the semiconductor device is not electrically conducted to the circuit board. In the case where an organic board such as a glass epoxy board is used for light weight and low cost, the corrosion of the aluminum electrode becomes more serious because the organic board is generally permeable to water and a lot of impurity ions are present in the organic board.

In order to solve the above-mentioned problems in the prior art, it is an object of the present invention to provide an electrode structure for a semiconductor device and a method for forming the electrode structure and a mounted body including the semiconductor device in which the semiconductor device can be easily connected to a circuit board reliably.

To accomplish the above-mentioned object, the present invention provides an electrode structure for a semiconductor device which is suitable for mounting on a circuit board in the face-down state, comprising a bump electrode formed on an aluminum electrode of the semiconductor device; and an aluminum oxide film sufficient for the prevention of corrosion of the aluminum electrode, which is formed on a surface of the aluminum electrode exposed around the bump electrode. According to the electrode structure for the semiconductor device, the aluminum electrode is not corroded due to water and impurity ions when mounting the semiconductor device on the circuit board.

In the electrode structure for a semiconductor device according to the present invention, it is preferable that the aluminum oxide film for the prevention of

corrosion should be obtained by further oxidizing a natural oxide film on the surface of the aluminum electrode.

In the electrode structure for a semiconductor device according to the present invention, it is preferable that the thickness of the aluminum oxide film should be 5 to 20 % of the thickness of the aluminum electrode. According to the preferred example, a practical electrode structure for the semiconductor device can be obtained. In other words, the thickness of the aluminum oxide film which is smaller than 5 % of the thickness of the aluminum electrode is not practical because it is very hard to form the aluminum oxide film densely. Furthermore, the thickness of the aluminum oxide film which is greater than 20 % of the thickness of the aluminum electrode is not practical because the resistance value of the aluminum electrode portion is increased too much.

In the electrode structure for a semiconductor device according to the present invention, it is preferable that the thickness of the aluminum oxide film should be 0.05 to 0.2  $\mu\text{m}$ . According to the preferred example, a practical electrode structure for the semiconductor device can be obtained. In general, the thickness of the aluminum electrode is about 1  $\mu\text{m}$ . Consequently, the aluminum oxide film having a thickness of 5 to 20 % of the thickness of the aluminum electrode can be obtained.

In the electrode structure for a semiconductor device according to the present invention, it is preferable that the bump electrode should be made of Au. According to the preferred example, the surface of the bump electrode is not oxidized at the step of forming an oxide film on the aluminum electrode of the semiconductor device. Accordingly, the bump electrode made of Au is convenient for the following bonding step. The reason is that if an oxide film is formed between the bonding layer and the surface of the bump electrode, a good electrical conduction cannot be obtained.

The present invention provides a method for forming an electrode structure for a semiconductor device which is mounted on a circuit board in the face-down state, comprising the steps of forming a bump electrode on an aluminum electrode of the semiconductor device; and forming an aluminum oxide film sufficient for the prevention of corrosion on a surface of the aluminum electrode that is exposed around the bump electrode. According to the method for forming the electrode structure for a semiconductor device, it is possible to obtain a semiconductor device in which the aluminum oxide film for the prevention of corrosion is formed on the surface of the aluminum electrode. As a result, the aluminum electrode is not corroded due to water and impurity ions when mounting the semiconductor device on the circuit board.

In the method for forming the electrode structure for a semiconductor device according to the present invention, it is preferable that the bump electrode should be formed by a wire bonding method using a Au wire. For example, the bump electrode is formed by the wire

bonding method in the following manner. First of all, the tip of the Au wire is fused by thermal energy to form a Au ball. Then, the Au ball formed on the tip of the Au wire is put on the aluminum electrode. Thereafter, a pressure of about 50 g is applied to the Au ball formed on the tip of the Au wire at a temperature of about 150°C so as to be bonded onto the aluminum electrode by pressure. Finally, the Au wire is pulled upward. After the above-mentioned steps, the bump electrode made of Au is formed on the aluminum electrode. According to the preferred example, the bump electrode can be formed directly on the aluminum electrode (a general electrode material) of the semiconductor device. Consequently, the bump electrode can be formed easily on the general purpose semiconductor device.

In the method for forming the electrode structure for a semiconductor device according to the present invention, it is preferable that the aluminum oxide film should be formed by exposing the semiconductor device having the bump electrode formed thereon to the high-temperature state. According to the preferred example, the aluminum oxide film can be formed efficiently in a short time. In this case, the high-temperature state should have a temperature of 200 to 300°C. According to the preferred example, this temperature range does not have a bad influence on the semiconductor device itself, that is, disconnection of an internal wiring or the like. Consequently, the aluminum oxide film can be formed efficiently in a short time. Furthermore, it is preferable that the aluminum oxide film should be formed simultaneously at the step of forming the bump electrode at a temperature of 200 to 300°C. According to the preferred example, the steps of forming the electrode structure for the semiconductor device can be simplified.

In the method for forming the electrode structure for a semiconductor device according to the present invention, the aluminum oxide film should be formed by immersing the semiconductor device having the bump electrode formed thereon in ammonium persulfate or hydrogen peroxide. According to this preferred example, the aluminum oxide film can be formed efficiently in a short time.

The present invention provides a mounted body including a semiconductor device, comprising a circuit board having a terminal electrode and a semiconductor device mounted on the circuit board in the face-down state, a bump electrode formed on an aluminum electrode of the semiconductor device and an aluminum oxide film sufficient for the prevention of corrosion that is formed on the surface of the aluminum electrode exposed around the bump electrode, the bump electrode being electrically connected to the terminal electrode on the circuit board through a bonding layer. With the present mounted body including a semiconductor device, the aluminum electrode is not corroded due to water and impurity ions. As a result, there is no possibility that the semiconductor is not electrically conducted to the circuit board. Consequently, a mounted body including a semiconductor device having high reliability

can be obtained.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the circuit board should be formed from a board including an organic material. According to the preferred example, the following functions and effects can be obtained. Recently, a board containing an organic material such as a glass epoxy board, a paper phenol board or the like has been used as a circuit board for light weight and low cost. This kind of board is permeable to water and contains a lot of impurity ions therein. However, the aluminum oxide film for the prevention of corrosion is formed on the surface of the aluminum electrode that is exposed around the bump electrode. Consequently, the aluminum electrode is not corroded due to water and impurity ions. According to the preferred example, it is possible to prevent the corrosion of the aluminum electrode while obtaining a reduction in weight and cost of the semiconductor device.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the aluminum oxide film for the prevention of corrosion should be obtained by further oxidizing a natural oxide film on the surface of the aluminum electrode.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the bonding layer should be made of a conductive adhesive. The conductive adhesive is obtained by mixing an organic binder, which contributes to adhesion, and a conductive filler, which contributes to electric conduction. An epoxy resin, a polyimide resin and the like are used as the organic binder. Ag, Cu, Au, Pd, Ni and the like having a mean volume particle diameter of 2 to 5  $\mu\text{m}$  are used as a conductive filler. In the case where the epoxy resin is used as the organic binder, the content of the conductive filler is 60 to 70 % by weight. An amine hardening agent or a phenol hardening agent is used. In the case where the polyimide resin is used as the organic binder, the content of the conductive filler is 60 to 90 % by weight and the hardening temperature is about 200°C. According to the preferred example, the thermal stress on the junction caused by the mismatching of physical property values (in particular, coefficients of thermal expansion) of the semiconductor device and the circuit board is relaxed. Thus, the mounted body including a semiconductor device can be obtained with high reliability. The reason is that the conductive adhesive has excellent relaxation ability in stress because it is generally softer than an inorganic material such as a solder.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the bonding layer should be made of a solder. According to the preferred example, the semiconductor device can be mounted at the same steps as the mounting of parts other than the semiconductor device, for example, a chip resistor, a chip capac-

itor and the like. As a result, the semiconductor device can also be mounted at the reflow soldering step together with other parts than the semiconductor device. Consequently, the process can be simplified.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that a gap between the semiconductor device and the circuit board should be filled with an insulating resin. An example of the insulating resin is an epoxy resin. According to the preferred example, the junction of the semiconductor device and the semiconductor device itself can be protected. In particular, the influence of water under high humidity conditions, such as the degradation of the junction or the failure of the semiconductor device, can be eliminated.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the thickness of the aluminum oxide film should be 5 to 20 % of the thickness of the aluminum electrode.

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the thickness of the aluminum oxide film should be 0.05 to 0.2  $\mu\text{m}$ .

In the structure for the mounted body including a semiconductor device according to the present invention, it is preferable that the bump electrode should be made of Au.

The present invention provides a semiconductor device suitable for mounting on a circuit board, comprising an aluminum electrode; a bump electrode formed on the aluminum electrode; and an aluminum oxide film sufficient for the prevention of corrosion of the aluminum electrode, which is formed on a surface of the aluminum electrode exposed around the bump electrode.

According to the electrode structure for the semiconductor device of the present invention, the aluminum electrode is not corroded due to water and impurity ions when mounting the semiconductor device on the circuit board.

According to the method for forming the electrode structure for the semiconductor device of the present invention, the semiconductor device in which the aluminum oxide film is formed on the surface of the aluminum electrode is obtained. As a result, the aluminum electrode is not corroded due to water and impurity ions when mounting the semiconductor device on the circuit board.

According to the mounted body including the semiconductor device of the present invention, the aluminum electrode is not corroded due to water and impurity ions. As a result, possibility that the semiconductor device is not electrically conducted to the circuit board is greatly reduced. Consequently, it is possible to obtain the mounted body including the semiconductor device which is very stable and reliable.

Figure 1 is a sectional view showing an electrode structure for a semiconductor device according to

an embodiment of the present invention;

Figure 2 is a sectional view showing a mounted body including the semiconductor device according to the embodiment of the present invention;

Figure 3 is a sectional view showing an electrode structure for the semiconductor device according to the prior art; and

Figure 4 is a sectional view showing a mounted body including the semiconductor device according to the prior art.

A preferred embodiment of the present invention will be described below.

Fig. 1 is a sectional view showing an electrode structure for a semiconductor device according to an embodiment of the present invention. Fig. 2 is a sectional view showing a mounted body including the semiconductor device according to the embodiment of the present invention.

As shown in Fig. 1, an aluminum electrode 2 is formed on an IC substrate 1. A passivation film 5 made of  $\text{SiO}_2$  is formed on the IC substrate 1 so as to cover the peripheral portion of the aluminum electrode 2. A bump electrode 3 is formed on the aluminum electrode 2 by a wire bonding method. The aluminum electrode 2 has an aluminum oxide film 4 formed on the surface which is exposed around the bump electrode 3. If the aluminum oxide film 4 is formed on the surface of the aluminum electrode 2 that is exposed around the bump electrode 3, the aluminum electrode 2 that is not corroded due to water and impurity ions when the semiconductor device is mounted on the circuit board. The bump electrode 3 is generally formed by the wire bonding method at a comparatively low temperature of about  $150^\circ\text{C}$ . The IC substrate 1 having about 100 aluminum electrodes 2 only passes through a heat history at a temperature of  $150^\circ\text{C}$  for a very short time of about 10 secs. For this reason, a natural oxide film formed on the surface of the aluminum electrode 2 during wire bonding has a thickness of about  $0.01\ \mu\text{m}$  and is porous, and is not sufficient to prevent the corrosion of the aluminum electrode 2.

It is preferable that the thickness of the aluminum oxide film 4 should be 5 to 20 % of the thickness of the aluminum electrode 2. In general, the thickness of the aluminum electrode 2 of the semiconductor device is about  $1\ \mu\text{m}$ . In this case, it is sufficient that the thickness of the aluminum oxide film 4 is about  $0.1\ \mu\text{m}$ . If the aluminum oxide film 4 is dense (there is no pin hole), the thickness of the aluminum oxide film 4 may be smaller, that is, about  $0.05\ \mu\text{m}$ . The thickness of the aluminum oxide film 4 which is smaller than 5 % of the thickness of the aluminum electrode 2, that is,  $0.05\ \mu\text{m}$ , is not practical because it is very hard to form the aluminum oxide film 4 densely. Furthermore, the thickness of the aluminum oxide film 4 which is greater than 20 % of the thickness of the aluminum electrode 2, that is,  $0.2\ \mu\text{m}$ , is not practical because the resistance value of the aluminum electrode portion is increased too much.

By way of example, the aluminum oxide film 4 can be formed by exposing the IC substrate 1 having the bump electrode 3 provided thereon to the high-temperature state (for example,  $300^\circ\text{C}$ ) in the air. More specifically, the aluminum oxide film 4 can be formed by a thermal oxidation method. Thus, the aluminum oxide film 4 can be formed efficiently in a short time by using the thermal oxidation method. In this case, it is preferable that the high-temperature state should have a temperature of 200 to  $300^\circ\text{C}$ .

It is preferable that the bump electrode 3 should be made of Au. The bump electrode 3 made of Au is convenient for the following bonding step because the surface of the bump electrode 3 is not oxidized at the step of forming an oxide film on the aluminum electrode 2. This is important because if an oxide film is formed between the bonding layer and the surface of the bump electrode 3, a good electric conduction cannot be obtained. In this case, the bump electrode 3 can be directly formed on the aluminum electrode (a general electrode material) 2 by the wire bonding method using a Au wire in the same manner as in the present embodiment. Consequently, the bump electrode can be easily formed on a general purpose semiconductor device. In general, the bump electrode is formed by a plating method. In this case, a thin film such as an adherence layer or a diffusion preventive layer is formed on the aluminum electrode and the bump electrode is then formed on the thin film. For this reason, the complicated process and apparatus are required.

The formation of the bump electrode 3 by the wire bonding method will be briefly described below. First of all, the tip of the Au wire is fused by thermal energy to form a Au ball. Then, the Au ball formed on the tip of the Au wire is put on the aluminum electrode. Thereafter, a pressure of about 50 g is applied to the Au ball formed on the tip of the Au wire at a temperature of about  $150^\circ\text{C}$  so as to be bonded onto the aluminum electrode by pressure. Finally, the Au wire is pulled upward. After these steps, the bump electrode 3 made of Au is formed on the aluminum electrode 2. By setting a heating temperature to the high-temperature state during wire bonding, that is, 200 to  $300^\circ\text{C}$ , the bump electrode 3 can be formed on the aluminum electrode 2 and the aluminum oxide film 4 can be formed on the surface of the aluminum electrode 2 that is exposed around the bump electrode 3. If the bump electrode 3 and the aluminum oxide film 4 are formed at the same time, the step of forming the electrode structure for the semiconductor device can be simplified.

The method for manufacturing the mounted body including the semiconductor device will be described below. As shown in Fig. 2, a conductive adhesive 8 is applied as a bonding layer to the tip portion of the bump electrode 3 of the semiconductor device having the above-mentioned structure by a transfer method or a printing method. Then, the semiconductor device is aligned in the face-down state in such a manner that the bump electrode 3 abuts on the terminal electrode 7 of

the circuit board 6. Then, the semiconductor device is mounted on the circuit board 6. When the conductive adhesive 8 is hardened in this state, the IC substrate 1 can be electrically connected to the circuit board 6. Finally, a gap between the IC substrate 1 and the circuit board 6 is filled with an insulating resin 9 such as an epoxy resin so that the mounted body including the semiconductor device can be obtained. By filling the gap between the IC substrate 1 and the circuit board 6 with the insulating resin 9, the junction between the board and the IC substrate 1 and the IC substrate 1 itself can be protected. In particular, it is possible to eliminate the influence of water at a high humidity, that is, the degradation of the junction and the failure of the semiconductor device. In the mounted body including the semiconductor device, the above-mentioned IC substrate 1, that is, the IC substrate 1 in which the surface is exposed around the bump electrode 3, is used and has the aluminum oxide film 4 formed thereon. Therefore, the aluminum electrode 2 is not corroded due to water and impurity ions. As a result, there is a greatly reduced possibility that the IC substrate 1 is not electrically conducted to the circuit board 6. Consequently, it is possible to obtain the mounted body including the semiconductor device with high reliability. In addition, it is possible to use, for the circuit board 6, an organic board such as a glass epoxy board which is permeable to water and contains a lot of impurity ions. Consequently, the weight and size of the semiconductor device can be reduced. An epoxy resin which is generally used as the insulating resin 9 is closely bonded to the metal surface by causing a hydroxyl group in the resin to generate hydrogen bonding with an oxide on the metal surface. By using a structure in which the aluminum oxide film 4 is formed on the surface of the aluminum electrode 2 exposed around the bump electrode 3 as in the present embodiment, the adhesion strength on an interface between the insulating resin 9 and the aluminum electrode 2 is enhanced. Thus, the sealing effect of the insulating resin 9 can be enhanced still more.

If the conductive adhesive 8 is used as a bonding layer, the thermal stress on the junction caused by the mismatching of physical property values (in particular, coefficients of thermal expansion) of the IC substrate 1 with the circuit board 6 is relaxed so that the mounted body including the semiconductor device can be obtained with high reliability. The reason is that the conductive adhesive has excellent relaxation ability in stress because it is softer than an inorganic material such as a solder. The conductive adhesive is obtained by mixing an organic binder which contributes to adhesion with a conductive filler which contributes to electric conduction. An epoxy resin, a polyimide resin and the like are used as the organic binder. Ag, Cu, Au, Pd, Ni and the like which have a mean volume particle diameter of 2 to 5  $\mu\text{m}$  are used as the conductive filler. If the epoxy resin is used for the organic binder, the content of the conductive filler is 60 to 70 % by weight and amine and phenol hardening agents are used. If the polyimide

resin is used as the organic binder, the content of the conductive filler is 60 to 90 % by weight and the hardening temperature is about 200°C.

In the present embodiment, the bump electrode 3 is formed by the wire bonding method, which should not be construed as being restricted. If the bump electrode 3 is directly formed on the aluminum electrode 2, an electroless plating method may be used.

While the conductive adhesive 8 is formed as the bonding layer on the bump electrode 3 by the transfer method or the printing method, the bonding layer instead may be formed on the terminal electrode 7 of the circuit board 6 in advance.

While the conductive adhesive 8 is used as the bonding layer in the present embodiment, other bonding materials such as a solder may be used. In particular, if the solder is used as the bonding layer, the semiconductor device can be mounted at the same steps as the mounting of parts other than the semiconductor device, for example, a chip resistor, a chip capacitor and the like. As a result, the semiconductor device can collectively be mounted at the reflow soldering step together with parts other than the semiconductor device. Consequently, the process can be simplified.

While the aluminum oxide film 4 is formed by the thermal oxidation method in the present embodiment, another method for oxidizing the surface of the aluminum electrode 2 may be used. As one example, the semiconductor device having the bump electrode 3 formed thereon is immersed in ammonium persulfate or hydrogen peroxide so as to form the aluminum oxide film 4.

While the semiconductor device which is mounted on the circuit board in the face-down state has been described as an example in the present embodiment, the present invention can be applied to the case where other mounting methods are used.

#### Claims

1. An electrode structure for a semiconductor device which is suitable for mounting on a circuit board in the face-down state, comprising:
  - a bump electrode formed on an aluminum electrode of said semiconductor device; and
  - an aluminum oxide film sufficient for the prevention of corrosion of the aluminum electrode, which is formed on a surface of said aluminum electrode exposed around said bump electrode.
2. The electrode structure for a semiconductor device as defined in Claim 1, wherein said aluminum oxide film for the prevention of corrosion is obtained by further oxidizing a natural oxide film on the surface of said aluminum electrode.
3. The electrode structure as defined in claim 1 or 2,



wherein the thickness of said aluminum oxide film is at least 5 to 20 % of the thickness of said aluminum electrode.

4. The electrode structure as defined in claim 1, 2 or 3, wherein the thickness of said aluminum oxide film is 0.05 to 0.2  $\mu\text{m}$ .

5. The electrode structure as defined in any of claims 1 to 4, wherein said bump electrode is made of Au.

6. A method for forming an electrode structure for a semiconductor device which is to be mounted on a circuit board in the face-down state, comprising the steps of:

forming a bump electrode on an aluminum electrode of said semiconductor device; and  
forming an aluminum oxide film sufficient for the prevention of corrosion on a surface of said aluminum electrode that is exposed around said bump electrode.

7. The method for forming an electrode structure for a semiconductor device as defined in Claim 6, wherein the bump electrode is formed by a wire bonding method using a Au wire.

8. The method for forming an electrode structure for a semiconductor device as defined in claim 6 or 7, wherein said aluminum oxide film is formed by exposing said semiconductor device having said bump electrode formed thereon to the high-temperature state.

9. The method for forming an electrode structure for a semiconductor device as defined in Claim 8, wherein the high-temperature state has a temperature of 200 to 300°C.

10. The method as defined in any of claims 6 to 9, wherein said aluminum oxide film is formed simultaneously at the step of forming the bump electrode at a temperature of 200 to 300°C.

11. The method as defined in any of claims 6 to 10, where said aluminum oxide film is formed by immersing said semiconductor device having said bump electrode formed thereon in ammonium persulfate or hydrogen peroxide.

12. A mounted body including a semiconductor device, comprising a circuit board having a terminal electrode and a semiconductor device mounted on said circuit board in the face-down state, a bump electrode formed on an aluminum electrode of said semiconductor device and an aluminum oxide film sufficient for the prevention of corrosion which is formed on the surface of said aluminum electrode

exposed around said bump electrode, the bump electrode being electrically connected to said terminal electrode on said circuit board through a bonding layer.

13. The mounted body including a semiconductor device as defined in Claim 12, wherein said circuit board is formed from a board including an organic material.

14. The mounted body including a semiconductor device as defined in claim 12 or 13; wherein said aluminum oxide film for the prevention of corrosion is obtained by further oxidizing a natural oxide film on the surface of said aluminum electrode.

15. The mounted body as defined in claim 12, 13 or 14, wherein said bonding layer is made of a conductive adhesive.

16. The mounted body as defined in any of claims 12 to 15, wherein said bonding layer is made of a solder.

17. The mounted body as defined in any of claims 12 to 16, wherein a gap between said semiconductor device and said circuit board is filled with an insulating resin.

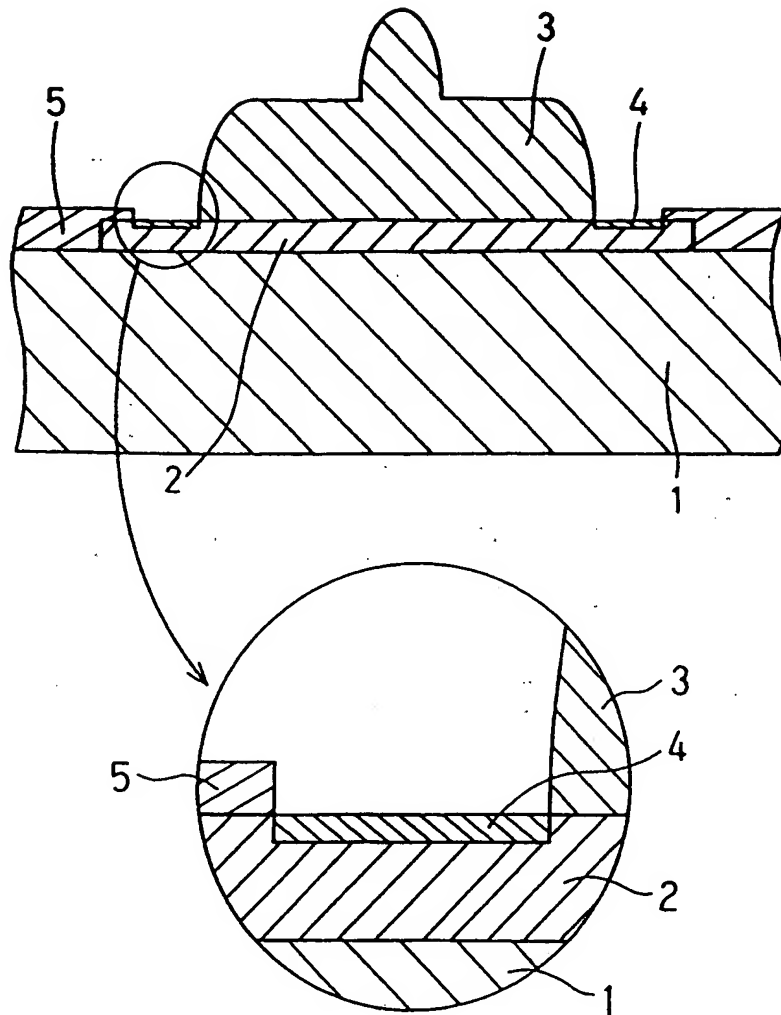
18. The mounted body as defined in any of claims 12 to 17, wherein the thickness of said aluminum oxide film is 5 to 20 % of the thickness of said aluminum electrode.

19. The mounted body as defined in any of claims 12 to 18, wherein the thickness of said aluminum oxide film is 0.05 to 0.2  $\mu\text{m}$ .

20. The mounted body as defined in any of claims 12 to 19, wherein said bump electrode is made of Au.

21. A semiconductor device suitable for mounting on a circuit board, comprising

an aluminum electrode;  
a bump electrode formed on said aluminum electrode; and  
an aluminum oxide film sufficient for the prevention of corrosion of said aluminum electrode, which is formed on a surface of said aluminum electrode exposed around said bump electrode.





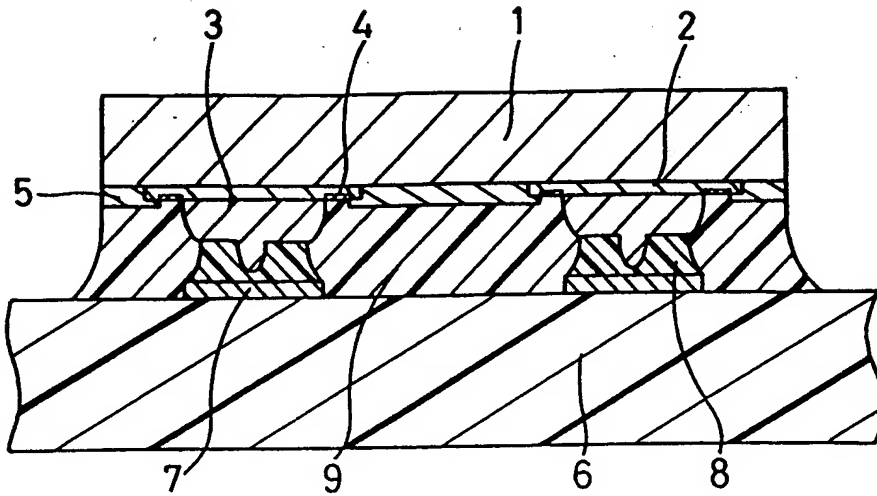


FIG. 2

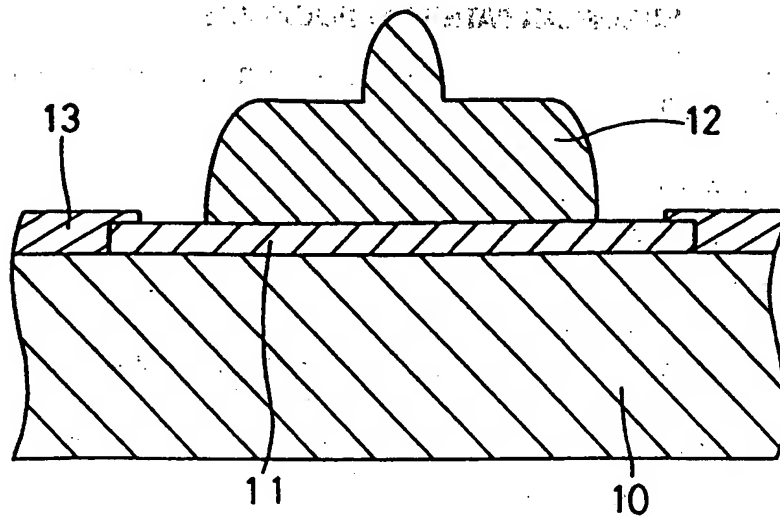


FIG. 3

(PRIOR ART)

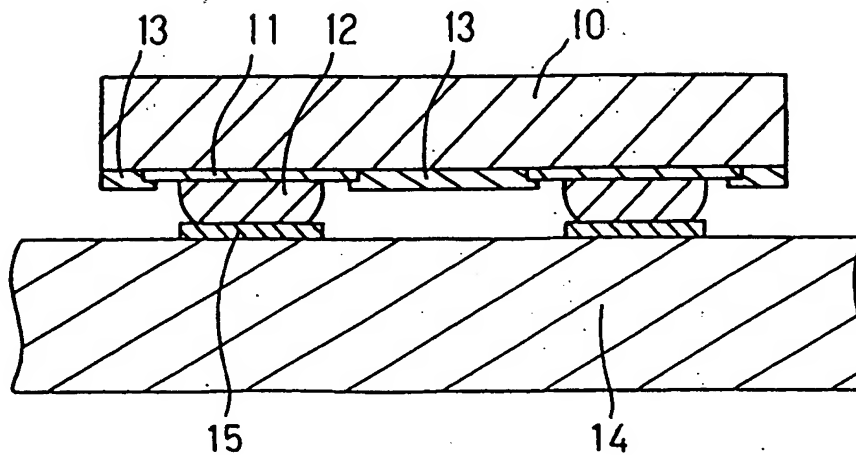
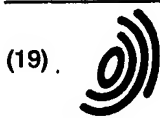


FIG. 4

(PRIOR ART)



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### (54) Electrode structure for semiconductor device, method for forming the same, and mounted body including semiconductor device

(57) An electrode structure for a semiconductor device and a method for forming the electrode structure, and a mounted body including the semiconductor device are provided in which the semiconductor device can be easily connected to a circuit board with high reliability. An aluminum electrode (2) is formed on an IC substrate (1). A passivation film (5) is formed on the IC substrate (1) so as to cover the peripheral portion of the aluminum electrode (2). A bump electrode (3) is formed on the aluminum electrode (2) by a wire bonding method. An aluminum oxide film (4) is formed on the surface of the aluminum electrode (2) that is exposed

around the bump electrode (3). A conductive adhesive (8) is applied as a bonding layer to the tip portion of the bump electrode (3) of the semiconductor device by a transfer method or a printing method. The semiconductor device is aligned in the face-down state in such a manner that the bump electrode (3) abuts on a terminal electrode (7) of a circuit board (6), and is provided on a circuit board (14). In this state, the conductive adhesive (8) is hardened. A gap between the IC substrate (1) and the circuit board (6) is filled with an insulating resin (9).

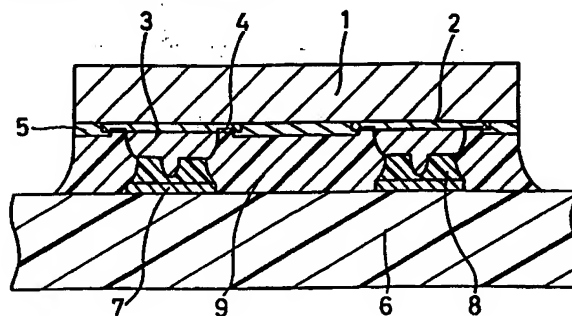


FIG. 2

EP 0 753 890 A3



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## EUROPEAN SEARCH REPORT

Application Number  
EP 96 11 1379

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	DE-A-43 03 401 (MITSUBISHI ELECTRIC CORP) 19 August 1993	1,2,4,6, 11,12, 14,19,21	H01L23/485 H01L21/60
Y	* column 2, line 45 - line 61; figure 23 *  * column 3, line 43 - line 57 * * column 10, line 65 - column 11, line 22; figures 15,16 * * column 12, line 36 - line 48; figure 22 * * & JP-A-63 269 541 (NEC CORP) 7 November 1988 ---	5,7,8, 15-17,20	
Y	PROCEEDINGS OF THE JAPAN INTERNATIONAL ELECTRONIC MANUFACTURING TECHNOLOGY SYMPOSIUM (JAPAN IEMT), KANAZAWA, JUNE 9 - 11, 1993, no. SYMP. 14, 9 June 1993, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, pages 362-365, XP000506078 YOSHIHIRO BESSHO ET AL: "A STUD-BUMP-BONDING TECHNIQUE FOR HIGH DENSITY MULTI-CHIP-MODULE" * page 362 * ---	5,7,15, 17,20	TECHNICAL FIELDS SEARCHED (Int.Cl.6)  H01L
Y	PATENT ABSTRACTS OF JAPAN vol. 015, no. 213 (E-1073), 30 May 1991 & JP-A-03 060035 (NEC CORP), 15 March 1991, * abstract *	8	
P,Y	---	16	
	PATENT ABSTRACTS OF JAPAN vol. 95, no. 010, 30 November 1995 & JP-A-07 176534 (NEC CORP), 14 July 1995, * abstract * ---		
-/--			
The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 17 December 1996	Examiner Le Minh, I
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons --- &: member of the same patent family, corresponding document	

EPO FORM 1503 01.92 (P04C01)



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

EP 96 11 1379

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	PATENT ABSTRACTS OF JAPAN vol. 008, no. 021 (E-224), 28 January 1984 & JP-A-58 184728 (TOKYO DENKI KAGAKU KOGYO KK), 28 October 1983, * abstract *	1,2	
A	---	6,12,14,21	
A	PATENT ABSTRACTS OF JAPAN vol. 007, no. 115 (E-176), 19 May 1983 & JP-A-58 033848 (TOKYO SHIBAURA DENKI KK), 28 February 1983, * abstract *	1,2,4-7,12,14,19-21	
A	PATENT ABSTRACTS OF JAPAN vol. 016, no. 134 (E-1185), 6 April 1992 & JP-A-03 295247 (FUJITSU LTD), 26 December 1991, * abstract *	1,2,6,11,12,14,21	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 17 December 1996	Examiner Le Minh, I
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 150 (01.02.1994)

1997, 1998, 1999, 2000, 2001, 2002, 2003, 2004, 2005, 2006, 2007, 2008, 2009, 2010, 2011, 2012, 2013, 2014, 2015, 2016, 2017, 2018, 2019, 2020, 2021, 2022, 2023, 2024, 2025, 2026, 2027, 2028, 2029, 2030, 2031, 2032, 2033, 2034, 2035, 2036, 2037, 2038, 2039, 2040, 2041, 2042, 2043, 2044, 2045, 2046, 2047, 2048, 2049, 2050, 2051, 2052, 2053, 2054, 2055, 2056, 2057, 2058, 2059, 2060, 2061, 2062, 2063, 2064, 2065, 2066, 2067, 2068, 2069, 2070, 2071, 2072, 2073, 2074, 2075, 2076, 2077, 2078, 2079, 2080, 2081, 2082, 2083, 2084, 2085, 2086, 2087, 2088, 2089, 2090, 2091, 2092, 2093, 2094, 2095, 2096, 2097, 2098, 2099, 2100, 2101, 2102, 2103, 2104, 2105, 2106, 2107, 2108, 2109, 2110, 2111, 2112, 2113, 2114, 2115, 2116, 2117, 2118, 2119, 2120, 2121, 2122, 2123, 2124, 2125, 2126, 2127, 2128, 2129, 2130, 2131, 2132, 2133, 2134, 2135, 2136, 2137, 2138, 2139, 2140, 2141, 2142, 2143, 2144, 2145, 2146, 2147, 2148, 2149, 2150, 2151, 2152, 2153, 2154, 2155, 2156, 2157, 2158, 2159, 2160, 2161, 2162, 2163, 2164, 2165, 2166, 2167, 2168, 2169, 2170, 2171, 2172, 2173, 2174, 2175, 2176, 2177, 2178, 2179, 2180, 2181, 2182, 2183, 2184, 2185, 2186, 2187, 2188, 2189, 2190, 2191, 2192, 2193, 2194, 2195, 2196, 2197, 2198, 2199, 2200, 2201, 2202, 2203, 2204, 2205, 2206, 2207, 2208, 2209, 2210, 2211, 2212, 2213, 2214, 2215, 2216, 2217, 2218, 2219, 2220, 2221, 2222, 2223, 2224, 2225, 2226, 2227, 2228, 2229, 2230, 2231, 2232, 2233, 2234, 2235, 2236, 2237, 2238, 2239, 2240, 2241, 2242, 2243, 2244, 2245, 2246, 2247, 2248, 2249, 2250, 2251, 2252, 2253, 2254, 2255, 2256, 2257, 2258, 2259, 2260, 2261, 2262, 2263, 2264, 2265, 2266, 2267, 2268, 2269, 2270, 2271, 2272, 2273, 2274, 2275, 2276, 2277, 2278, 2279, 2280, 2281, 2282, 2283, 2284, 2285, 2286, 2287, 2288, 2289, 2290, 2291, 2292, 2293, 2294, 2295, 2296, 2297, 2298, 2299, 2300, 2301, 2302, 2303, 2304, 2305, 2306, 2307, 2308, 2309, 2310, 2311, 2312, 2313, 2314, 2315, 2316, 2317, 2318, 2319, 2320, 2321, 2322, 2323, 2324, 2325, 2326, 2327, 2328, 2329, 2330, 2331, 2332, 2333, 2334, 2335, 2336, 2337, 2338, 2339, 2340, 2341, 2342, 2343, 2344, 2345, 2346, 2347, 2348, 2349, 2350, 2351, 2352, 2353, 2354, 2355, 2356, 2357, 2358, 2359, 2360, 2361, 2362, 2363, 2364, 2365, 2366, 2367, 2368, 2369, 2370, 2371, 2372, 2373, 2374, 2375, 2376, 2377, 2378, 2379, 2380, 2381, 2382, 2383, 2384, 2385, 2386, 2387, 2388, 2389, 2390, 2391, 2392, 2393, 2394, 2395, 2396, 2397, 2398, 2399, 2400, 2401, 2402, 2403, 2404, 2405, 2406, 2407, 2408, 2409, 2410, 2411, 2412, 2413, 2414, 2415, 2416, 2417, 2418, 2419, 2420, 2421, 2422, 2423, 2424, 2425, 2426, 2427, 2428, 2429, 2430, 2431, 2432, 2433, 2434, 2435, 2436, 2437, 2438, 2439, 2440, 2441, 2442, 2443, 2444, 2445, 2446, 2447, 2448, 2449, 2450, 2451, 2452, 2453, 2454, 2455, 2456, 2457, 2458, 2459, 2460, 2461, 2462, 2463, 2464, 2465, 2466, 2467, 2468, 2469, 2470, 2471, 2472, 2473, 2474, 2475, 2476, 2477, 2478, 2479, 2480, 2481, 2482, 2483, 2484, 2485, 2486, 2487, 2488, 2489, 2490, 2491, 2492, 2493, 2494, 2495, 2496, 2497, 2498, 2499, 2500, 2501, 2502, 2503, 2504, 2505, 2506, 2507, 2508, 2509, 2510, 2511, 2512, 2513, 2514, 2515, 2516, 2517, 2518, 2519, 2520, 2521, 2522, 2523, 2524, 2525, 2526, 2527, 2528, 2529, 2530, 2531, 2532, 2533, 2534, 2535, 2536, 2537, 2538, 2539, 2540, 2541, 2542, 2543, 2544, 2545, 2546, 2547, 2548, 2549, 2550, 2551, 2552, 2553, 2554, 2555, 2556, 2557, 2558, 2559, 2560, 2561, 2562, 2563, 2564, 2565, 2566, 2567, 2568, 2569, 2570, 2571, 2572, 2573, 2574, 2575, 2576, 2577, 2578, 2579, 2580, 2581, 2582, 2583, 2584, 2585, 2586, 2587, 2588, 2589, 2590, 2591, 2592, 2593, 2594, 2595, 2596, 2597, 2598, 2599, 2600, 2601, 2602, 2603, 2604, 2605, 2606, 2607, 2608, 2609, 2610, 2611, 2612, 2613, 2614, 2615, 2616, 2617, 2618, 2619, 2620, 2621, 2622, 2623, 2624, 2625, 2626, 2627, 2628, 2629, 2630, 2631, 2632, 2633, 2634, 2635, 2636, 2637, 2638, 2639, 2640, 2641, 2642, 2643, 2644, 2645, 2646, 2647, 2648, 2649, 2650, 2651, 2652, 2653, 2654, 2655, 2656, 2657, 2658, 2659, 2660, 2661, 2662, 2663, 2664, 2665, 2666, 2667, 2668, 2669, 2670, 2671, 2672, 2673, 2674, 2675, 2676, 2677, 2678, 26

$\frac{1}{2} \left( \frac{1}{2} \right)^{n-1} = \frac{1}{2^n}$

1. The first step in the process is to identify the problem or issue that needs to be addressed. This involves gathering information and understanding the context of the problem.

2. Once the problem is identified, the next step is to define the objectives and goals of the project. This helps to clarify what needs to be achieved and provides a clear direction for the team.

3. The third step is to develop a plan or strategy to address the problem. This involves breaking down the problem into smaller, manageable tasks and determining the resources needed to complete each task.

4. The fourth step is to implement the plan. This involves putting the strategy into action and monitoring progress to ensure that the project is on track.

5. The final step is to evaluate the results of the project. This involves assessing the outcomes against the objectives and goals and identifying any areas for improvement.